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PATENT

EXPEDITED PROCEDURE

RESPONSE UNDER 37 CFR 1.116 – EXAMINING GROUP 2811

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
PATENT APPLICATION

App./Confirm Nos.: 09/683,985 2597

Applicant: Steven Voldman

Filing Date: March 8, 2002

Title: A METHOD OF FORMING LOW
CAPACITANCE ESD ROBUST DIODES

Atty. Docket: BUR9-2002-0014-US1

Today's Date: September 14, 2005

Examiner: Ori Nadav

Group Art Unit: 2811

Fax: 571-273-8300

Amendments After Final Under 37 C.F.R 1.116
Amendment DCommissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Final Office Action dated June 14, 2005, Applicant respectfully requests reconsideration of the outstanding rejections and reexamination of the present application in light of the following remarks and amendments.

CERTIFICATE OF MAILING

I hereby certify that, on the date shown below, this correspondence is being:

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P.O. Box 1450, Alexandria, VA 22313-1450.

Date: 09/14/2005

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☒ transmitted by facsimile to the Patent and Trademark

ANTHONY J. CANALE

Name

Signature

BUR9-2002-0014-US1

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